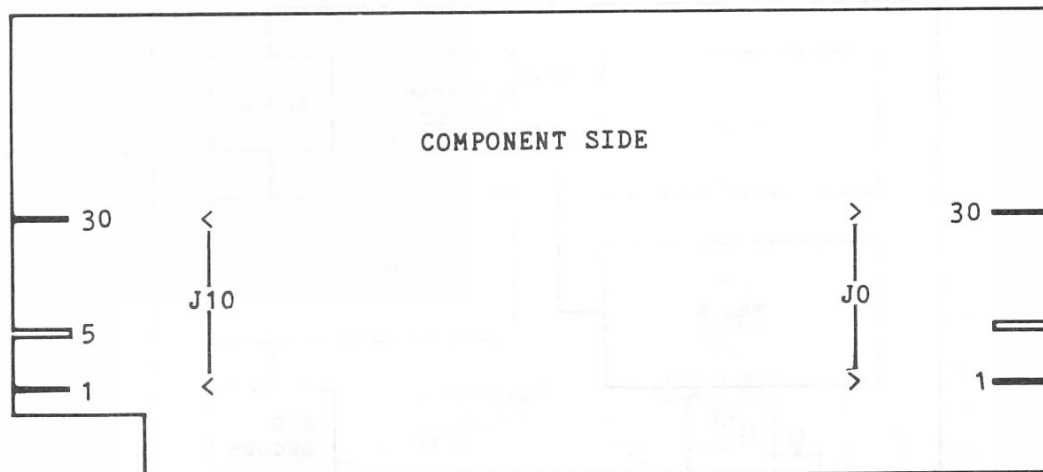
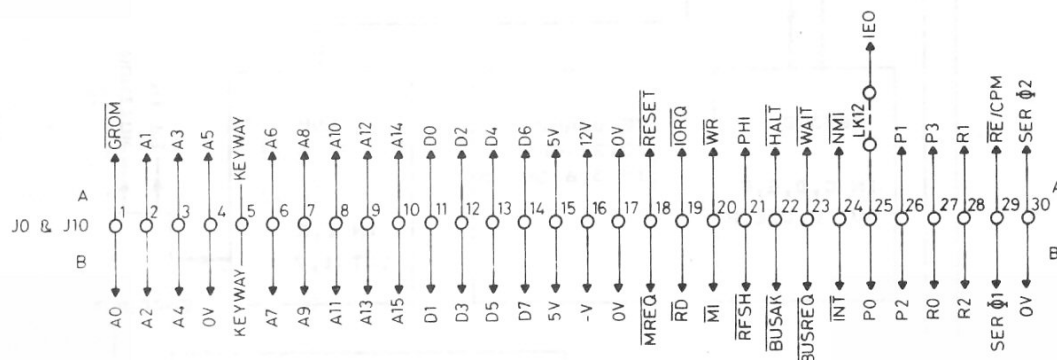


APPENDIX 3 MTX SERIES SYSTEM BUS



The system Bus comprises the full Z80A bus, power supply rails, ROMpak enable (GROM), ROM page ports R0 to R2, RAM page ports P0 to P3 and serial clock lines 01 and 02.

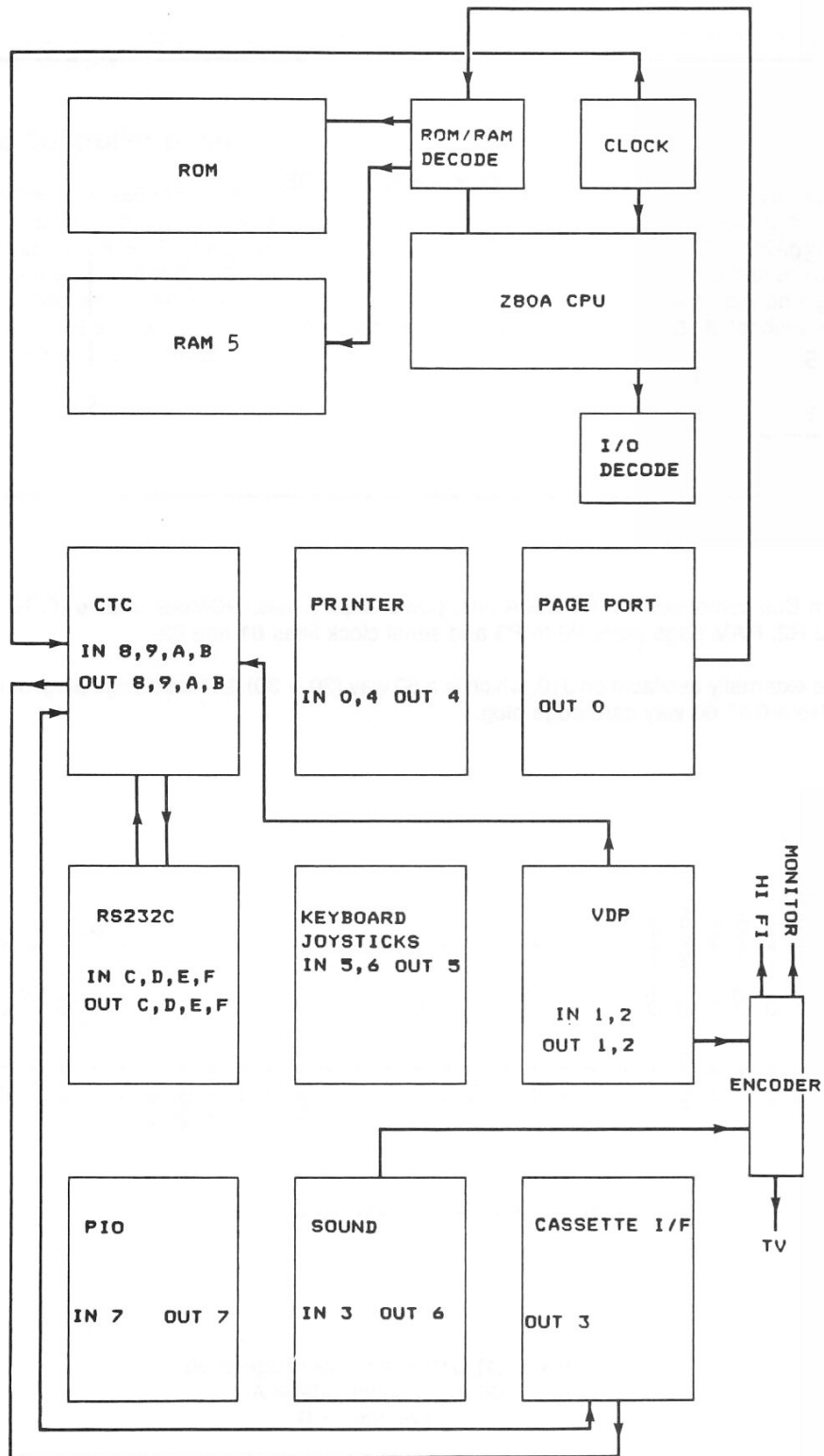
All lines are externally available on J10, which is a 60 way (30 + 30) 0.1" card edge plug, or internally on J0 which is also a 0.1" 60 way card edge plug.



NOTE :- J10 ALSO HAS KEYWAY BETWEEN 26 AND 27

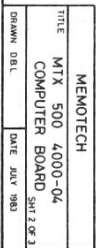
Note: (1) J10 is a mirror image of J0
 (2) component side = A
 Solder side = B

APPENDIX 4 SYSTEM BLOCK DIAGRAM

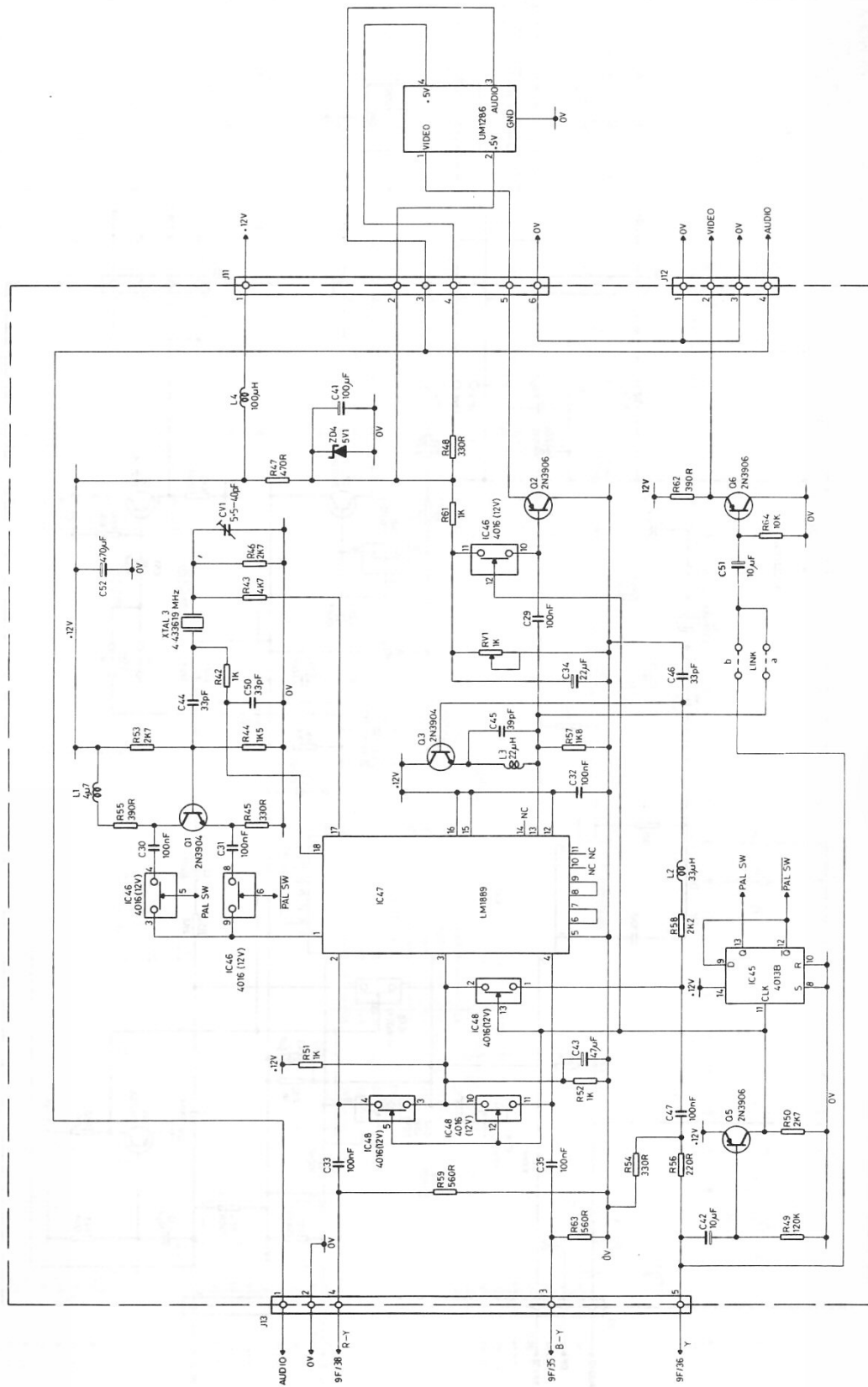


MTX 500 4000-04

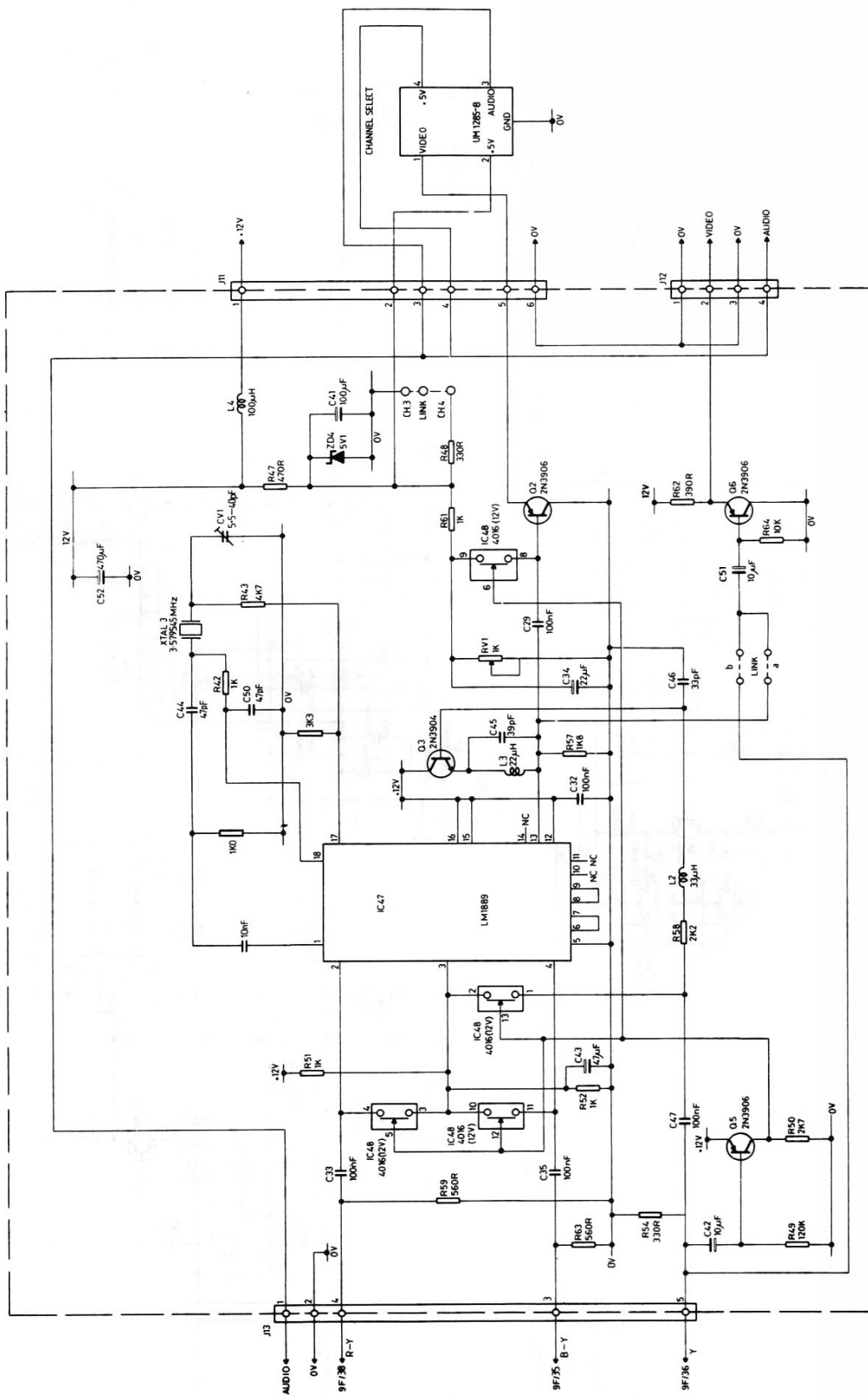




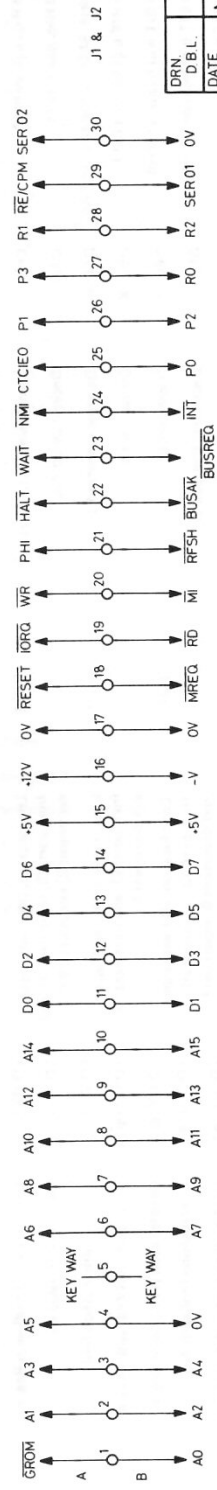
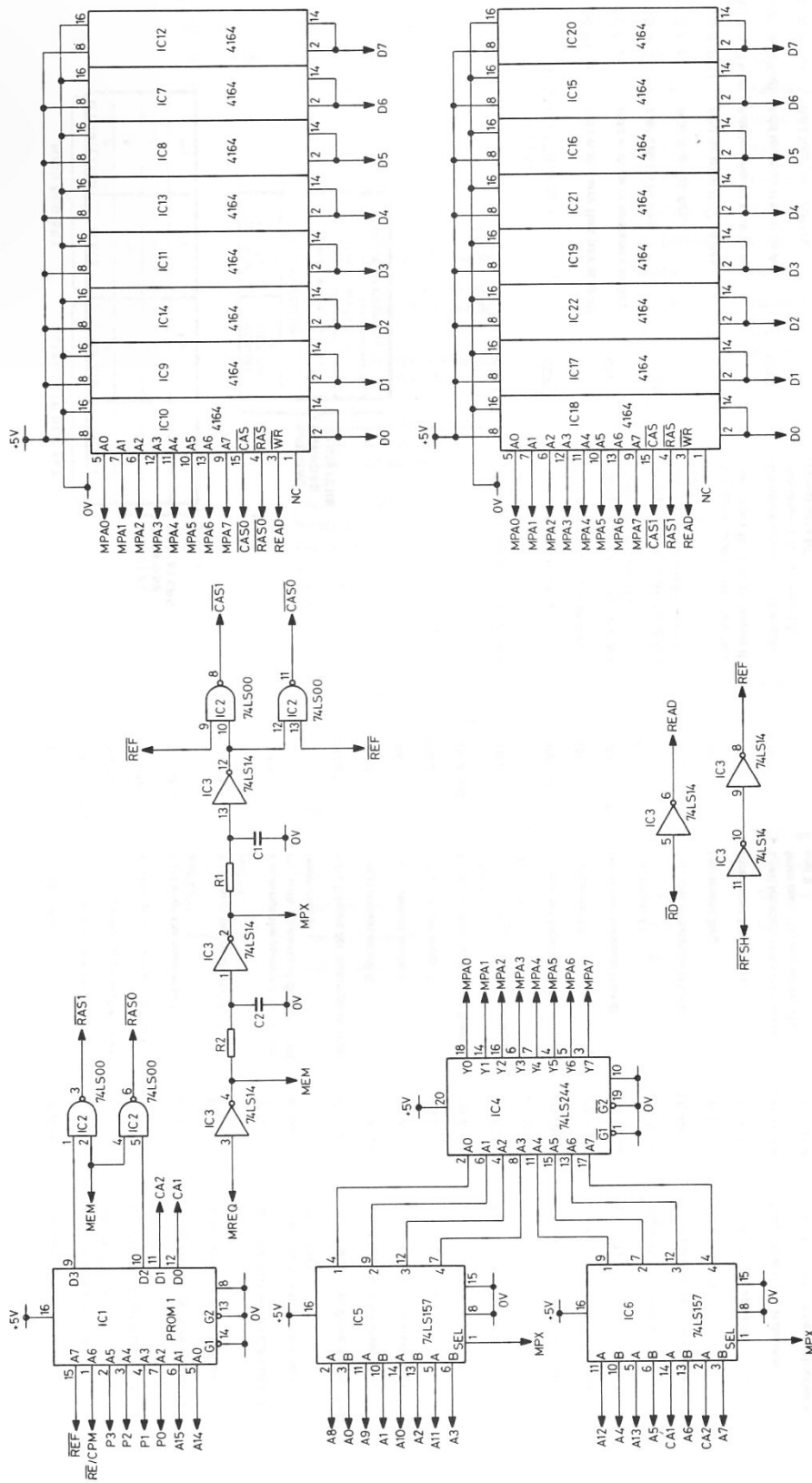
PAL BOARD



NTSC VIDEO BOARD



MEMORY EXPANSION BOARD



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DATE	MTX 500 MEMORY
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